

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,820,031 B1
DATED : November 16, 2004
INVENTOR(S) : T. Hayakawa

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12,

Line 57, “;” should be -- : --.

Column 13,

Line 14, after “system” insert -- circuit --.

Line 58, after “corrects” insert -- a --.

Column 14,

Line 30, “;” should be -- : --.

Column 15,

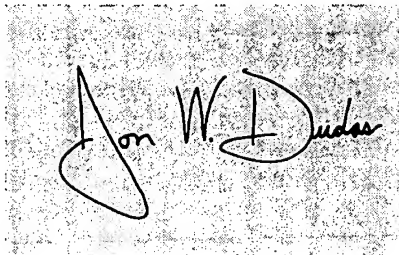
Line 45, after “timing” insert -- , --.

Column 16,

Lines 16 and 46, “;” should be -- : --.

Signed and Sealed this

Twenty-seventh Day of December, 2005

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular area with a light, textured background.

JON W. DUDAS
Director of the United States Patent and Trademark Office